

REMARKS/ARGUMENTS

Claims 1-22 are pending in the application; claim 1 has been amended. Reexamination and reconsideration are hereby requested.

Claims 1-2 were rejected as unpatentable over Conover in view of Wilson; Conover showed a PLL with VCO and Wilson added a DTO.

Applicant replies that claim 1 has been amended to point out that the PLL output is the vertical synchronization signal (i.e., Fig.1), not a detection. In contrast, Conover either blanks or passes the composite video input through; Conover's PLL just makes the decision. Thus Conover plus Wilson do not suggest amended claim 1.

For claims 2-22 applicant replies upon the patentability of parent claim 1.

Respectfully submitted,

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